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wherein said first signal has a reversed phase relation with said second signal.

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33. (Amended) A method of driving a display device comprising the steps of:
driving scanning lines of a display panel including a switching element for every pixel
electrode;

driving signal lines of said display panel;
controlling driving said display panel; and
producing a phase difference in a second signal with respect to a phase of a first signal
which is input to said signal line driving circuit or to said scanning line driving circuit,
wherein each of said first signal and said second signal is a clock signal, and
wherein a signal rise time period (t_r) or a signal fall time period (t_f) is equal to or
shorter than a half of a signal holding time period (t_c).

34. (Amended) A method according to claim 33, wherein said first signal has a
reversed phase relation with said second signal.

REMARKS

Applicant wishes to thank the Examiner for the very thorough consideration given the present application and for indicating the allowability of claims 4-6, 10-12, 17-19, 23-25, 29-31 and 35-37 if rewritten in independent form including all of the limitations of the base claim and any intervening claim. The Office Action of **June 5, 2001**, has been received and its contents carefully noted. Applicant respectfully submits that this response is timely filed.

Claims 1 and 3-38 were pending in the present application prior to the aforementioned amendment. By the above actions, claims 11, 18, 24 and 36 have been canceled without prejudice, and claims 1, 8, 14, 21, 27, 33 and 34 have been amended to clearly recite subject matter to which Applicant is already entitled. Applicant submits that no issue of new matter

is raised by this amendment and that said Amendment does not raise new issues which require a further consideration and/or search. Accordingly, claims 1, 3-10, 12-17, 19-23, 25-35 and 37-38 are currently pending in the present application and, at least for the reasons set forth below, are believed to be in condition for allowance.

Initially, the Office Action objects to claim 34 as being of improper dependent form. By the above Amendment, claim 34 has been amended to place the claim in proper dependent form. More particularly, claim 34 has been amended so that the recitation "wherein each of said first signal and said second signal is a clock signal" now recites -- wherein said first signal has a reversed phase relation with said second signal--. Because Applicant believes that the claim objection has been obviated, reconsideration and withdrawal is respectfully requested.

The Office Action rejects claims 1, 3 7-9, 13-16, 20-22, 26-28, 32-34 and 38 under 35 U.S.C. §102(e) as anticipated by *Aoki* (U.S. Patent No. 6,011,533). Applicant respectfully traverses this ground for rejection and reconsideration of the pending claims is respectfully requested for the reasons solicited below.

The claimed invention is directed to a display device including, *inter alia*, a circuit for producing a phase difference in a second signal with respect to a phase of a first signal which is input to the signal line driving circuit or to the scanning line driving circuit, wherein said first signal has a reversed phase relation with said second signal. As illustrated in Fig. 3, this claim feature is provided in order to generate a plurality of noises comprising a peak having a small amplitude.

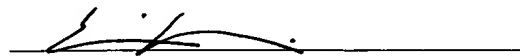
As the Examiner well knows, "a claim is anticipated only if each and every element as set forth in the claim is found, either expressly or inherently described, in a single prior art reference." *Verdegaal Bros. v. Union Oil Co. Of California*, 814 F.2d 628, 631, 2 USPQ2d 1051 (Fed. Cir. 1987). "The identical invention must be shown in as complete detail as is contained in the...claims." *Richardson v. Suzuki Motor Co.*, 868 F.2d 1226, 1236,

9 USPQ2d 1913 (Fed. Cir. 1989).

Applicant respectfully contends that the claims as presently set forth recite subject matter which is clearly patentably distinct over the prior art of record. More particularly, Applicant respectfully contends that the *Aoki* patent fails to expressly teach or inherently describe each and every limitation necessary to anticipate the claimed invention under §102. In particular, the Office Action finds that *Aoki* discloses an image display panel comprising line driving circuit (102) and a signal line driving circuit (110a, 110b) for driving signal lines of the liquid display panel. *Aoki* fails to expressly teach or inherently disclose, however, is an image display panel comprising a circuit for producing a phase difference in a second signal with respect to a phase of a first signal which is input to the signal line driving circuit or to the scanning line driving circuit, wherein said first signal has a reversed phase relation with said second signal. Thus, since *Aoki* fails to expressly teach or implicitly suggest all the claim limitations of the present invention, reconsideration of the pending claims and withdrawal of the rejection are solicited.

Accordingly, Applicant respectfully submits that the pending claims are in proper condition for allowance and consideration and withdrawal of the pending rejections are requested. If the Examiner believes further discussions with Applicant's representative would be beneficial in this case, he is invited to contact the undersigned.

Respectfully submitted,



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MARKED-UP COPY OF AMENDED CLAIMS

1. (Twice Amended) [An image] A display device comprising:
a [liquid crystal] display panel [having] comprising a switching element for every pixel electrode;
a scanning line driving circuit for driving scanning lines of said [liquid crystal] display panel;
a signal line driving circuit for driving signal lines of said [liquid crystal] display panel;
a control circuit for controlling driving said [liquid crystal] display panel;
a video signal processing circuit; and
a circuit for producing a phase difference in a second signal with respect to a phase of a first signal which is input to said signal line driving circuit or to said scanning line driving circuit,
wherein said first signal has a reversed phase relation with said second signal.

8. (Amended) [An image] A display device comprising:
a [liquid crystal] display panel [having] comprising a switching element for every pixel electrode;
a scanning line driving circuit for driving scanning lines of said [liquid crystal] display panel;
a signal line driving circuit for driving signal lines of said [liquid crystal] display panel;
a control circuit for controlling driving said [liquid crystal] display panel;
a video signal processing circuit; and
a circuit for producing a phase difference in a second signal with respect to a phase

of a first signal which is input to said signal line driving circuit or to said scanning line driving circuit,

wherein each of said first signal and said second signal is a clock signal, and
wherein a signal rise time period (t_r) or a signal fall time period (t_f) is equal to or shorter than a half of a signal holding time period (t_c).

14. (Amended) [An image] A display device comprising:
a [liquid crystal] display panel [having] comprising a switching element for every pixel electrode;
a scanning line driving circuit for driving scanning lines of said [liquid crystal] display panel;
a signal line driving circuit for driving signal lines of said [liquid crystal] display panel;
a control circuit for controlling driving said [liquid crystal] display panel;
a video signal processing circuit; and
a circuit for producing a phase difference in a second signal with respect to a phase of a first signal which is input to a shift register circuit, and
wherein a signal rise time period (t_r) or a signal fall time period (t_f) is equal to or shorter than a half of a signal holding time period (t_c).

21. (Amended) [An image] A display device comprising:
a [liquid crystal] display panel [having] comprising a switching element for every pixel electrode;
a scanning line driving circuit for driving scanning lines of said [liquid crystal] display panel;
a signal line driving circuit for driving signal lines of said [liquid crystal] display

panel;

a control circuit for controlling driving said [liquid crystal] display panel;

a video signal processing circuit; and

a circuit for producing a phase difference in a second signal with respect to a phase of a first signal which is input to a latch circuit, and

wherein a signal rise time period (t_r) or a signal fall time period (t_f) is equal to or shorter than a half of a signal holding time period (t_c).

27. (Amended) A driving method of [an image] a display device comprising the steps of:

driving scanning lines of a [liquid crystal] display panel including a switching element for every pixel electrode;

driving signal lines of said [liquid crystal] display panel;

controlling driving said [liquid crystal] display panel; and

producing a phase difference in a second signal with respect to a phase of a first signal which is input to said signal line driving circuit or to said scanning line driving circuit, wherein said first signal has a reversed phase relation with said second signal.

33. (Amended) A method of driving [an image] a display device comprising the steps of:

driving scanning lines of a [liquid crystal] display panel including a switching element for every pixel electrode;

driving signal lines of said [liquid crystal] display panel;

controlling driving said [liquid crystal] display panel; and

producing a phase difference in a second signal with respect to a phase of a first signal which is input to said signal line driving circuit or to said scanning line driving circuit,

wherein each of said first signal and said second signal is a clock signal, and
wherein a signal rise time period (t_r) or a signal fall time period (t_f) is equal to or
shorter than a half of a signal holding time period (t_c).

34. (Amended) A method according to claim 33, wherein [each of said first signal and said second signal is a clock signal] said first signal has a reversed phase relation with said second signal.